

Gamma-ray Large Area Space Telescope



A Partnership in Astrophysics and Particle Physics

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University of California at Santa Cruz

Presentation to SLUO
July 15, 1998

GLAST



GLAST Collaboration, July 1998

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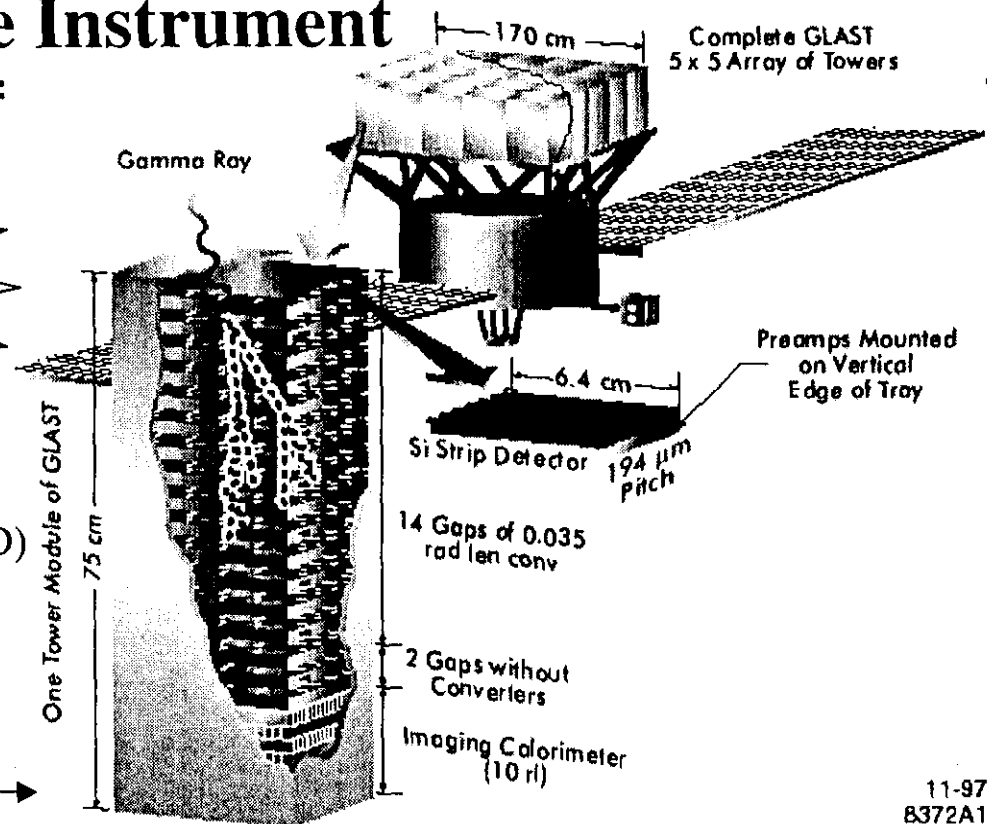


GLAST Baseline Instrument

**Pair Conversion
Telescope followed
by E-M calorimeter**

- Scintillator anti-coincidence detector (ACD)
- Silicon strip tracker (0.7 r.l. distributed)
- Imaging Cs I calorimeter (10 r.l.)

Single Tower →



- Almost 2π detector, looking away from the earth in scanning mode → all sky monitor.
- Modular in 25 identical towers → redundancy to avoid catastrophic failures.
- No consumables → long mission lifetime (> 5 years).
- Robust and well understood technologies → no surprises!
- Low detector noise → high efficiency, self triggering.
- Flexible programmable HEP type trigger → changing and multifaceted physics goals.



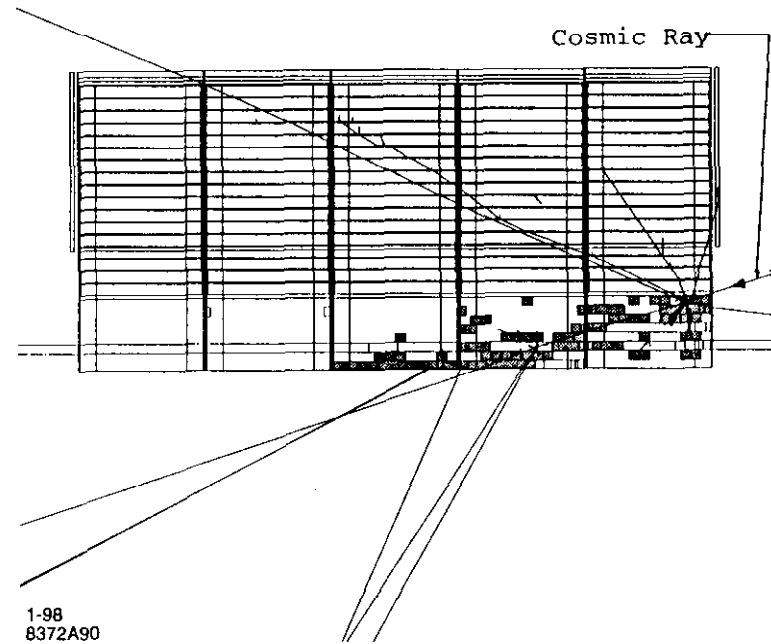
GLAST R&D Program Overview

- *Simulation studies for optimization and verification of the conceptual design.*
- *Construction of a prototype tower over the next 18 months.*
- *Beam tests at SLAC and CERN.*
- *Electrical, mechanical and thermal design for the full GLAST instrument.*
- *DAQ system design for the full GLAST instrument.*



Simulations

- The GLAST baseline concept has been thoroughly investigated and verified by detailed Monte Carlo simulation.
- Two years of work was put into this *before* any significant investment was made in hardware development.
- Summary:
 - Cosmic-ray rejection of $\approx 5 \cdot 10^5:1$ with 80% gamma-ray efficiency. Signal/Noise $>20::1$ for extragalactic diffuse gammas.
 - Solid predictions for effective area and resolutions (now verified by beam tests).
 - Workable scheme for triggering.
 - Design optimization.



An example simulation of a 15-GeV cosmic ray proton striking the GLAST calorimeter.

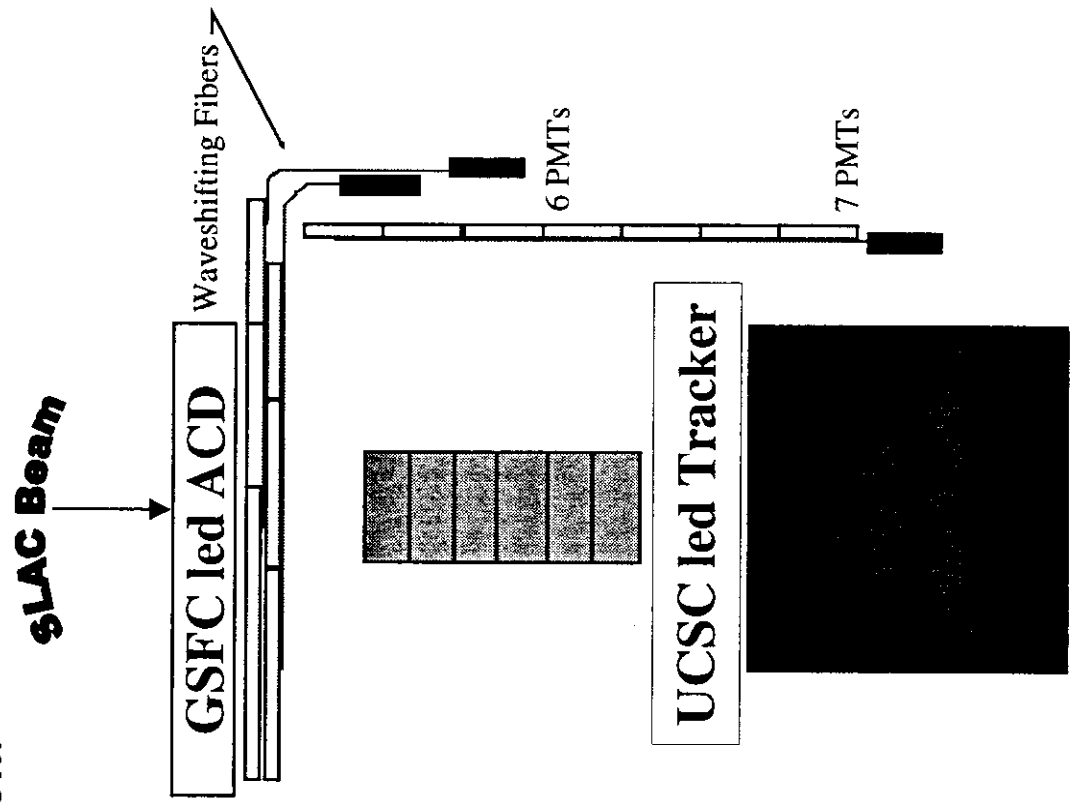
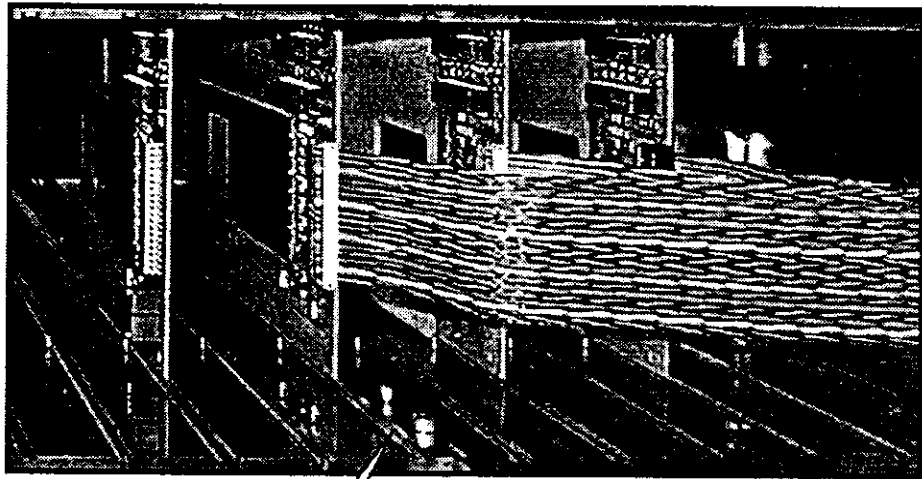


October 1997 Beam Test at SLAC

*Broad participation across the collaboration.
Test of prototype components of the
GLAST baseline design.*

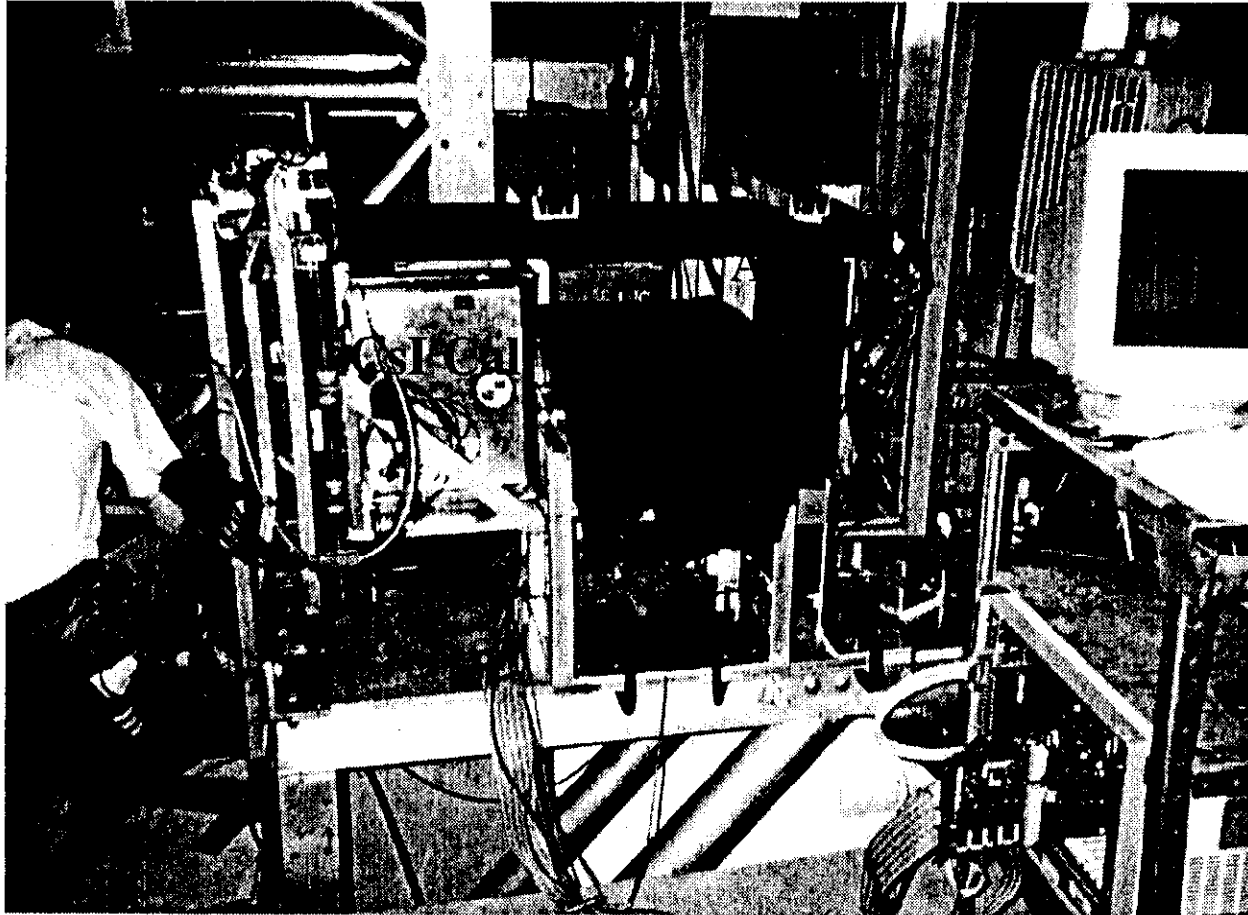
UCSC led SSD tracker with 6 x,y planes and interchangeable converter foils

NRL led Hadrosopic CsI Calorimeter





October 1997 Beam Test: SLAC-ESA

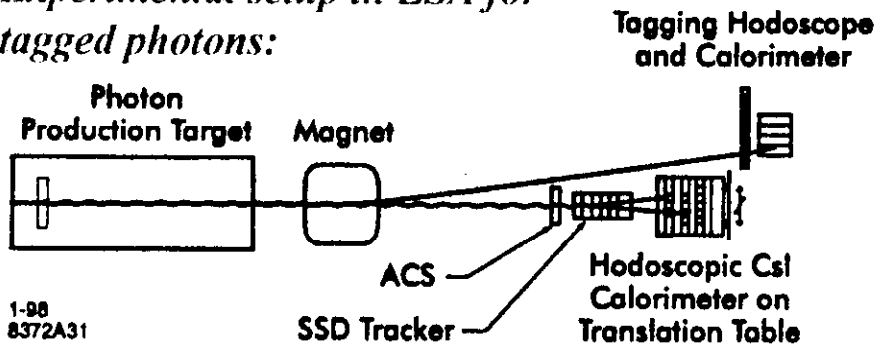


All elements of a prototype tower in *miniature* designed to play together.



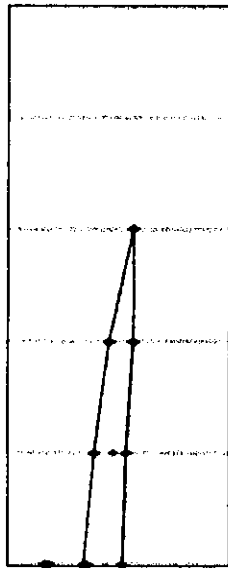
GLAST Beam Test, Tracker Performance

Experimental setup in ESA for tagged photons:

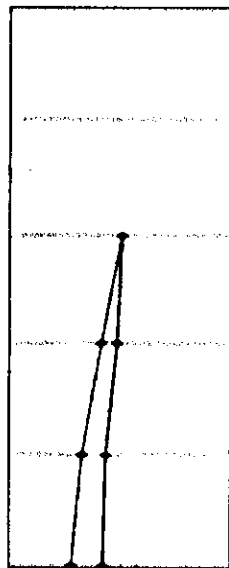


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3-cm spacing, 400 MeV photon



X Projection

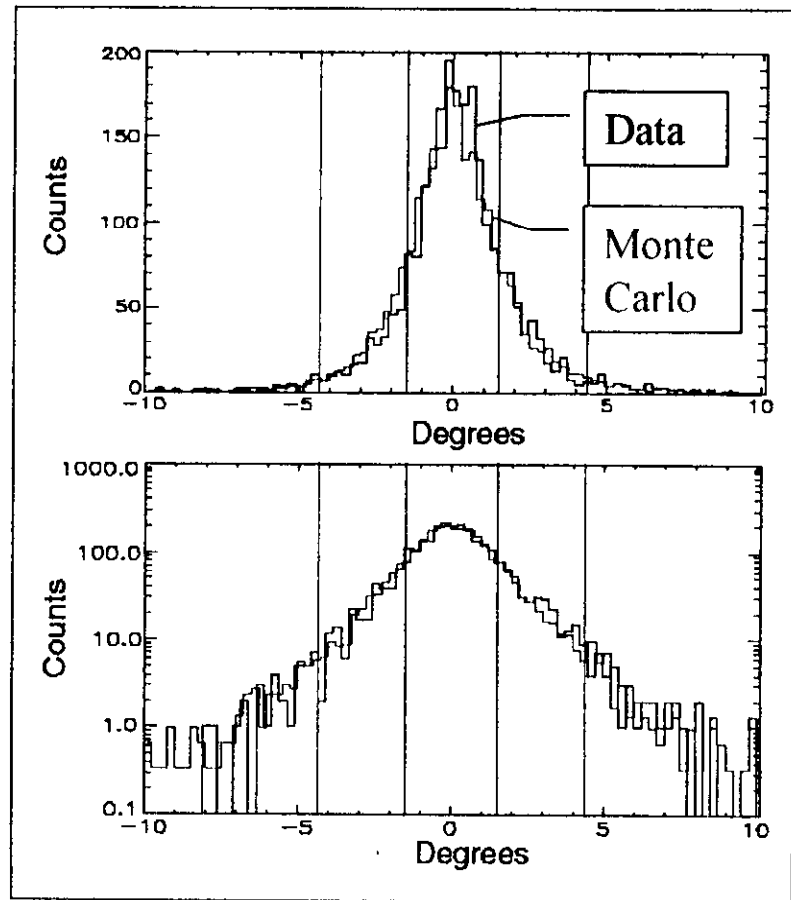


Y Projection

To scale

400 MeV

X Projected Angle
3-cm spacing, 4% foils, 100-200 MeV

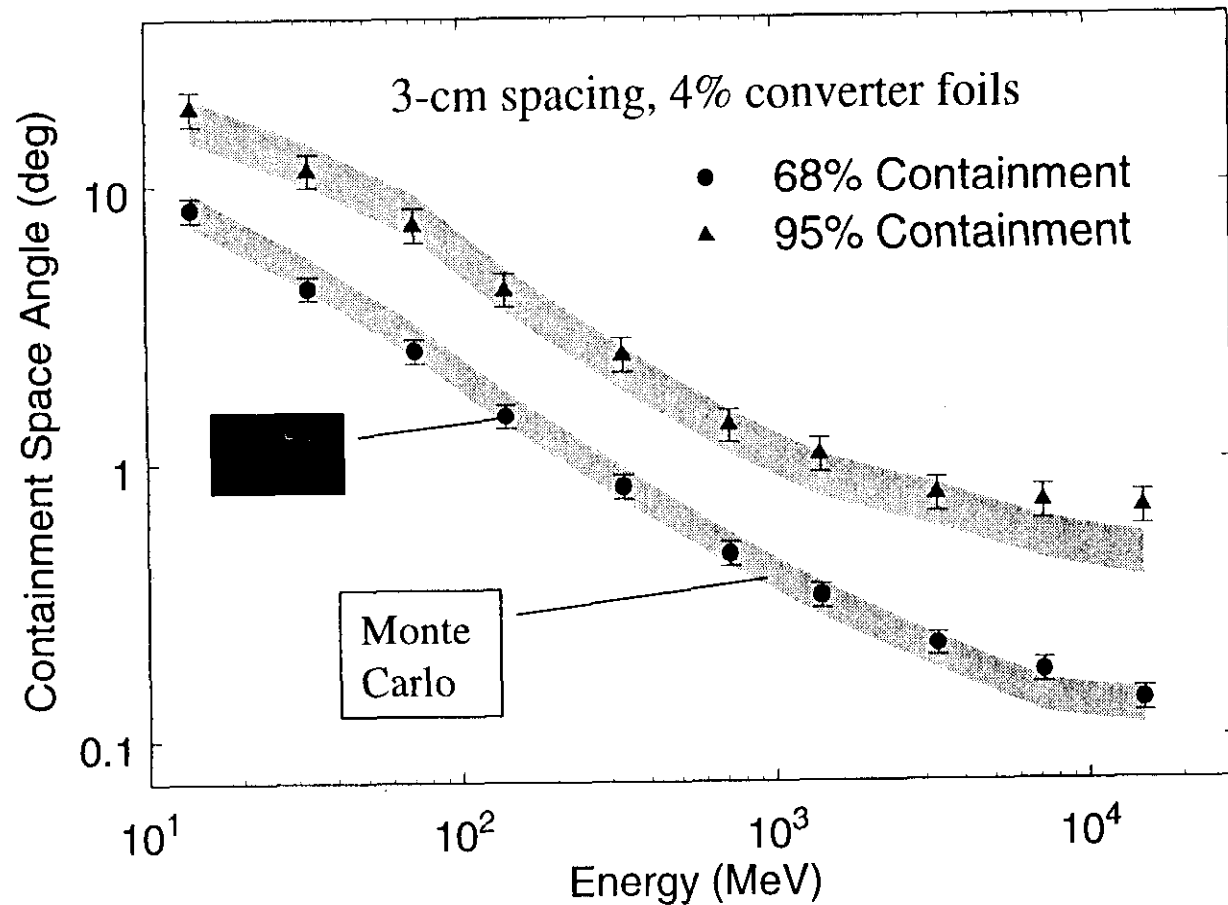




GLAST Beam Test, Tracker Performance

*Excellent agreement
of data with MC*

Tagged Photons

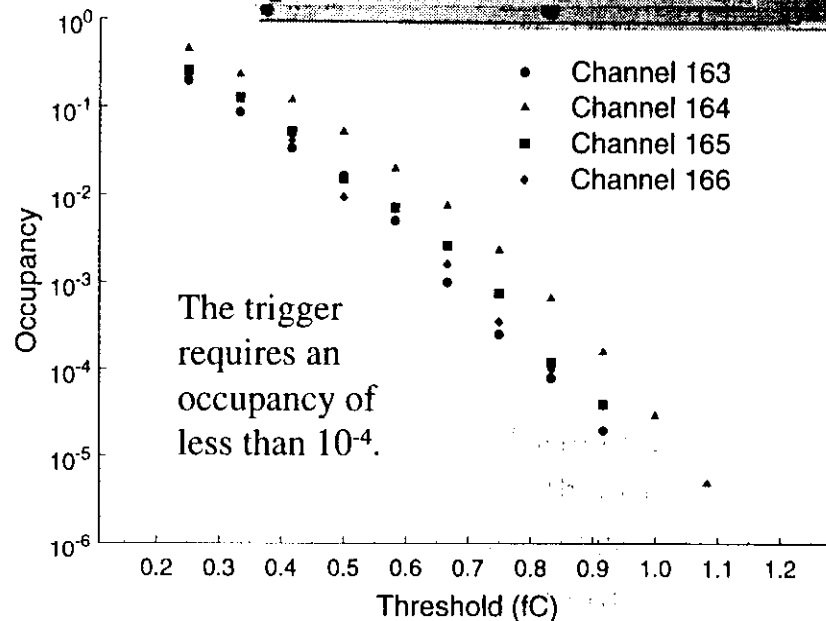
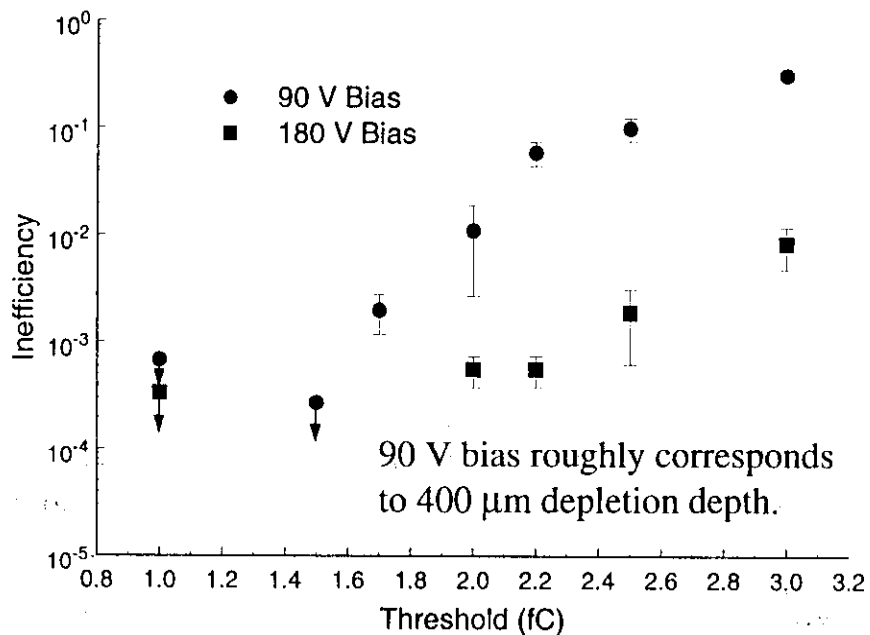
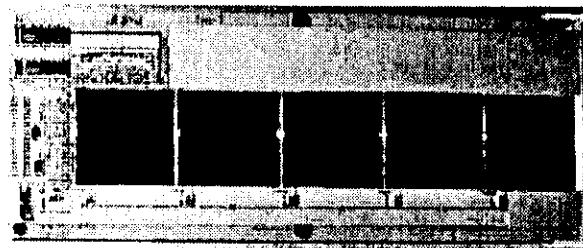


(Note that the performance shown differs from that of the full-scale device, due to severe edge effects in the beam test tracker.)



GLAST Beam Test, Tracker Performance

Tracker electronics performance measured with a 30 cm long detector ladder.

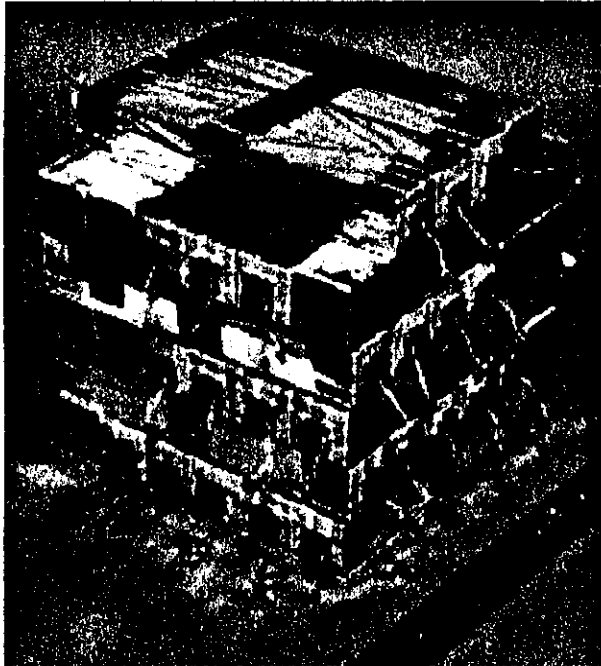


Inefficiency for detection of minimum ionizing electrons at normal incidence in a single detector plane. The nominal threshold used is 1.5 fC.

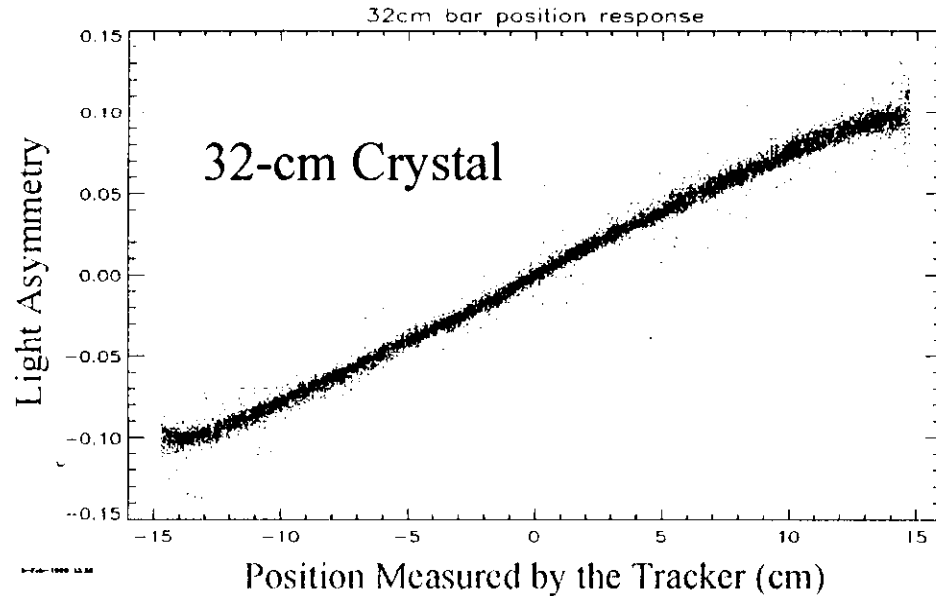
Noise occupancy, in a 1 μs window, versus threshold for four different channels connected to 30 cm long detector strips.



GLAST Beam Test, Calorimeter Performance



Imaging Calorimeter Concept



- Hodoscopic CsI calorimeter stack.
- 24 19-cm long CsI crystals.
- PIN diode readout on each end of each crystal, for measurement of the position from light asymmetry.
- A single 32-cm long crystal was also tested.

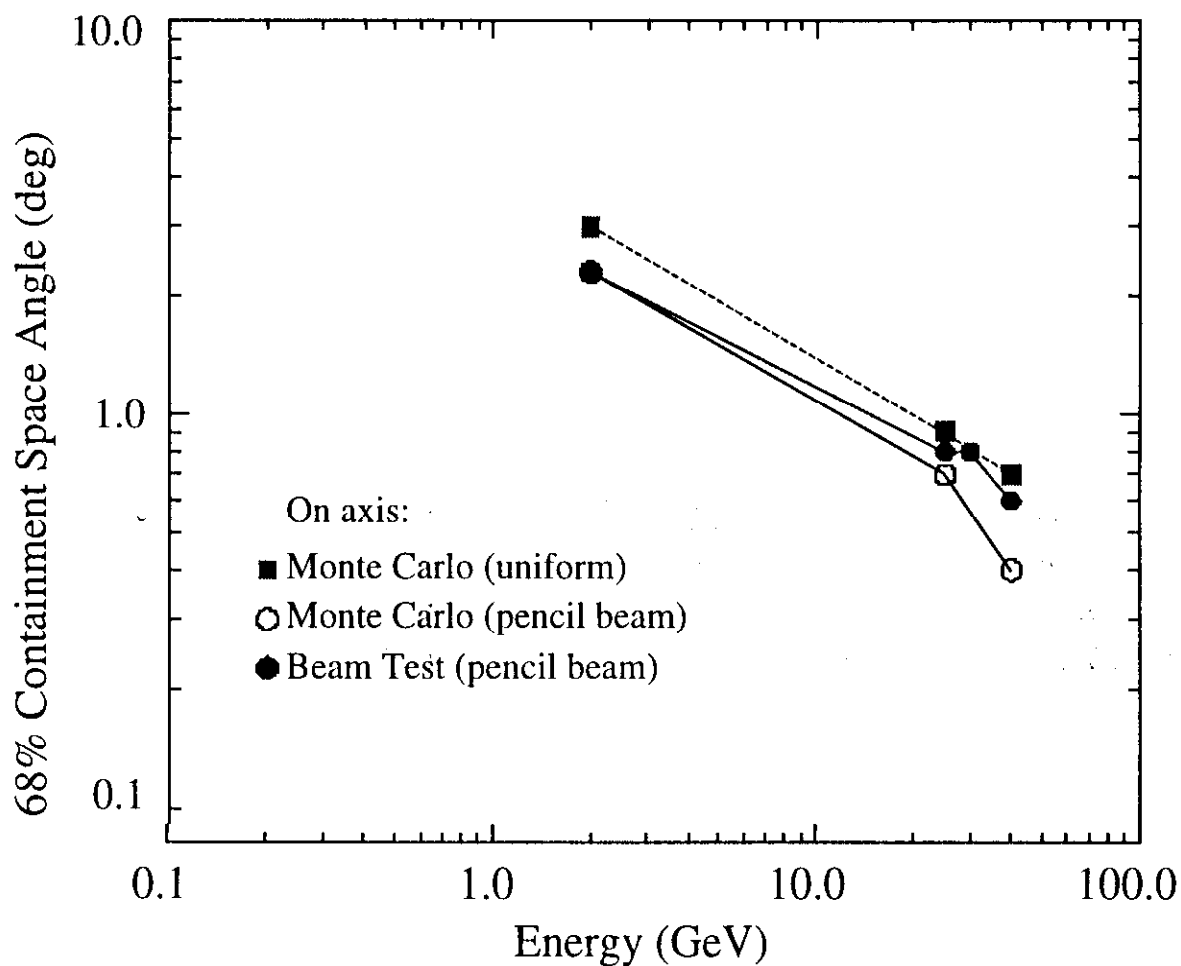


GLAST Beam Test, Calorimeter Performance

Calorimeter-Only Angular Resolution

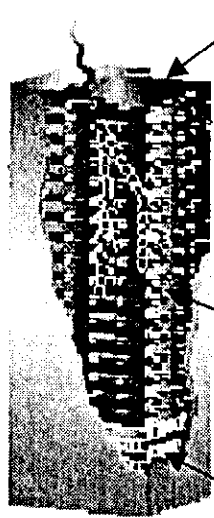
In the high-energy range, we can achieve better than 1° angular resolution on photons that do not convert in the tracker, more than doubling the effective area.

The beam-test data validate the Monte Carlo simulation.





Prototype Tower Design

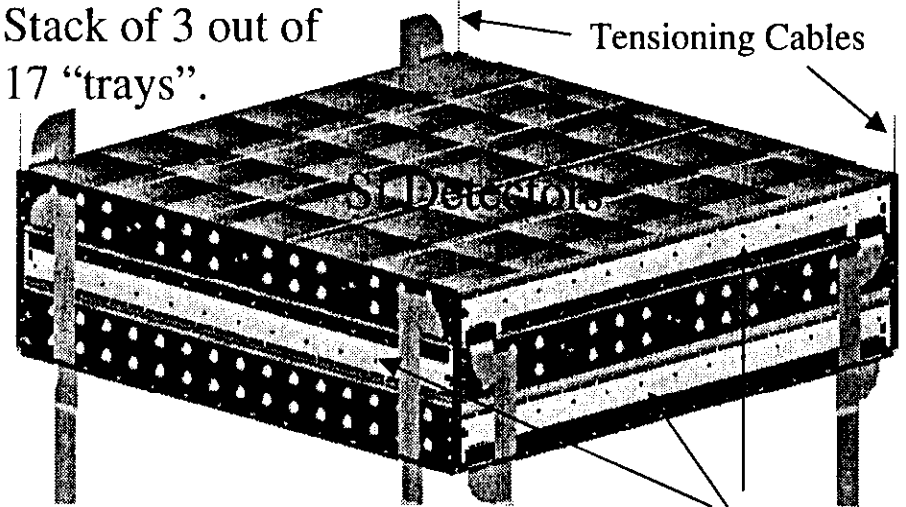


ACD

Mechanical design is by collaboration of Hytec Inc., SLAC, NRL, and GSFC.

Tracker

Stack of 3 out of 17 "trays".

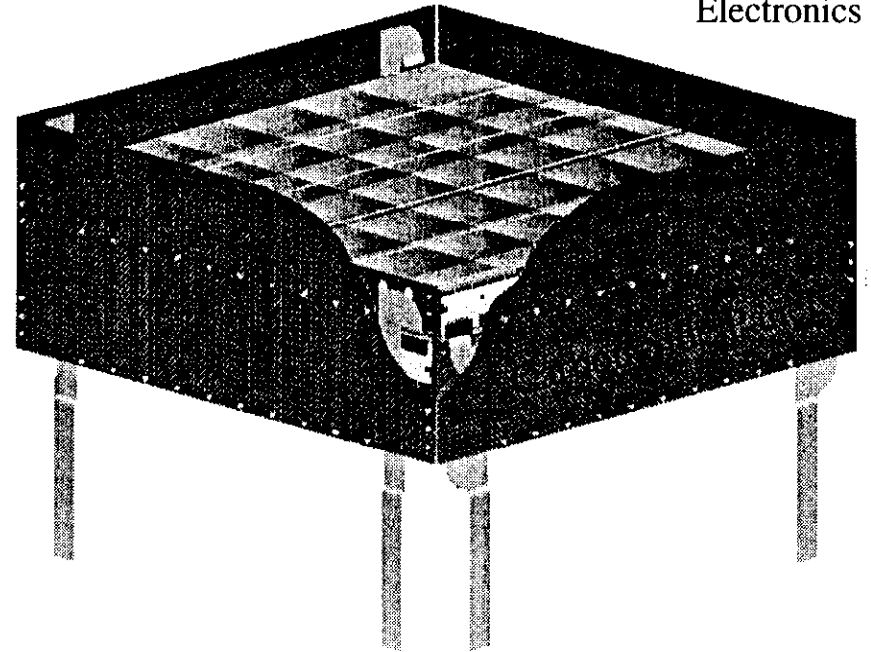
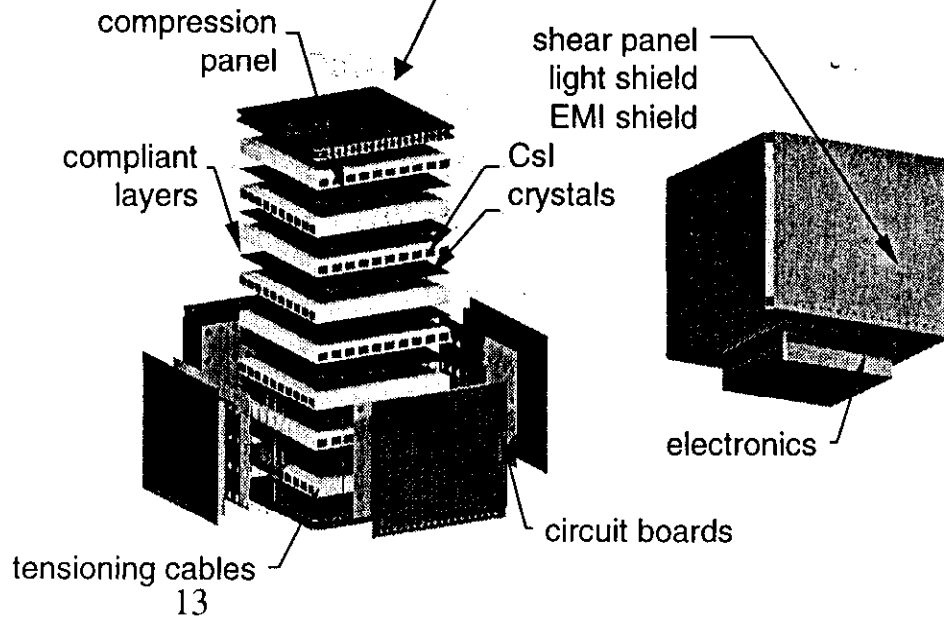


Tensioning Cables

Si Detectors

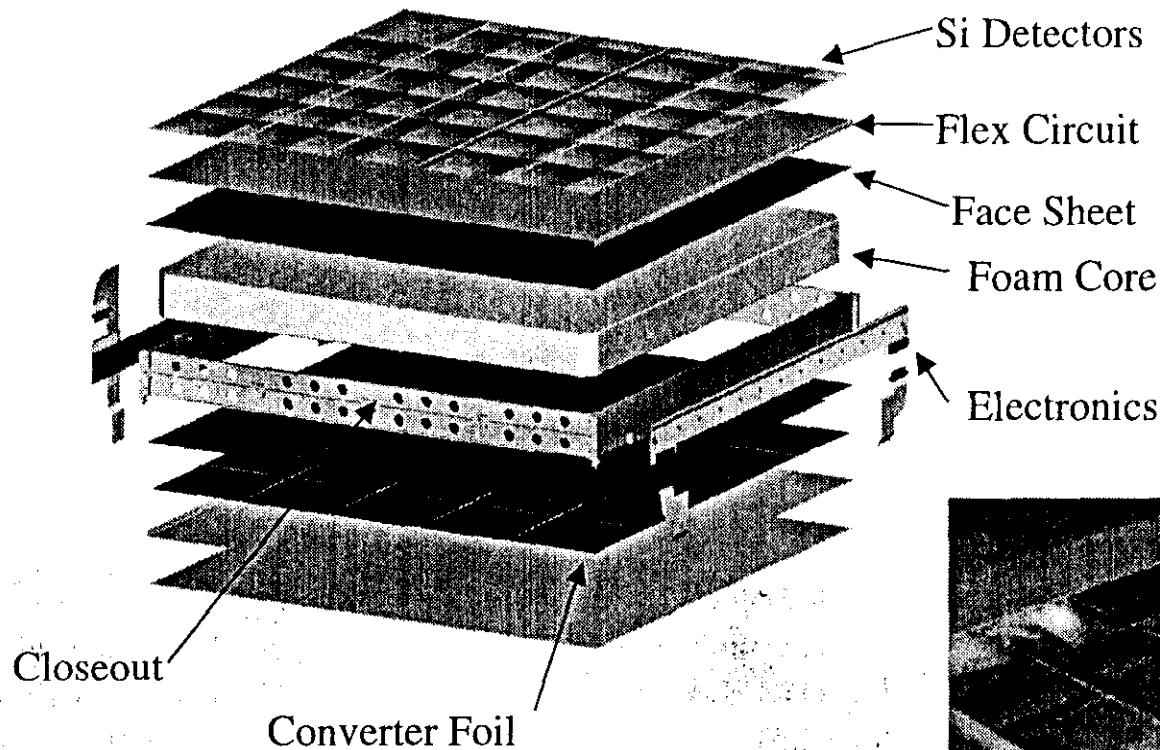
Electronics

Calorimeter



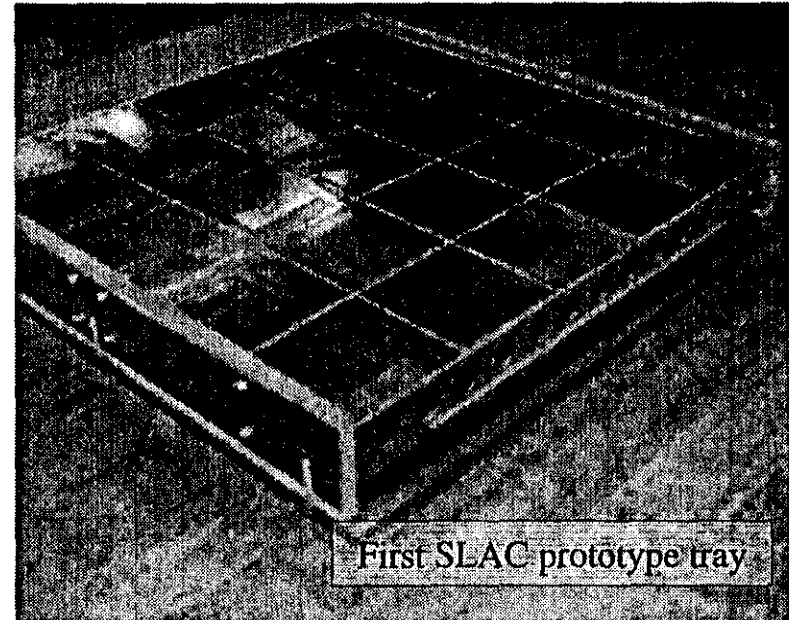


Tracker Tray Design



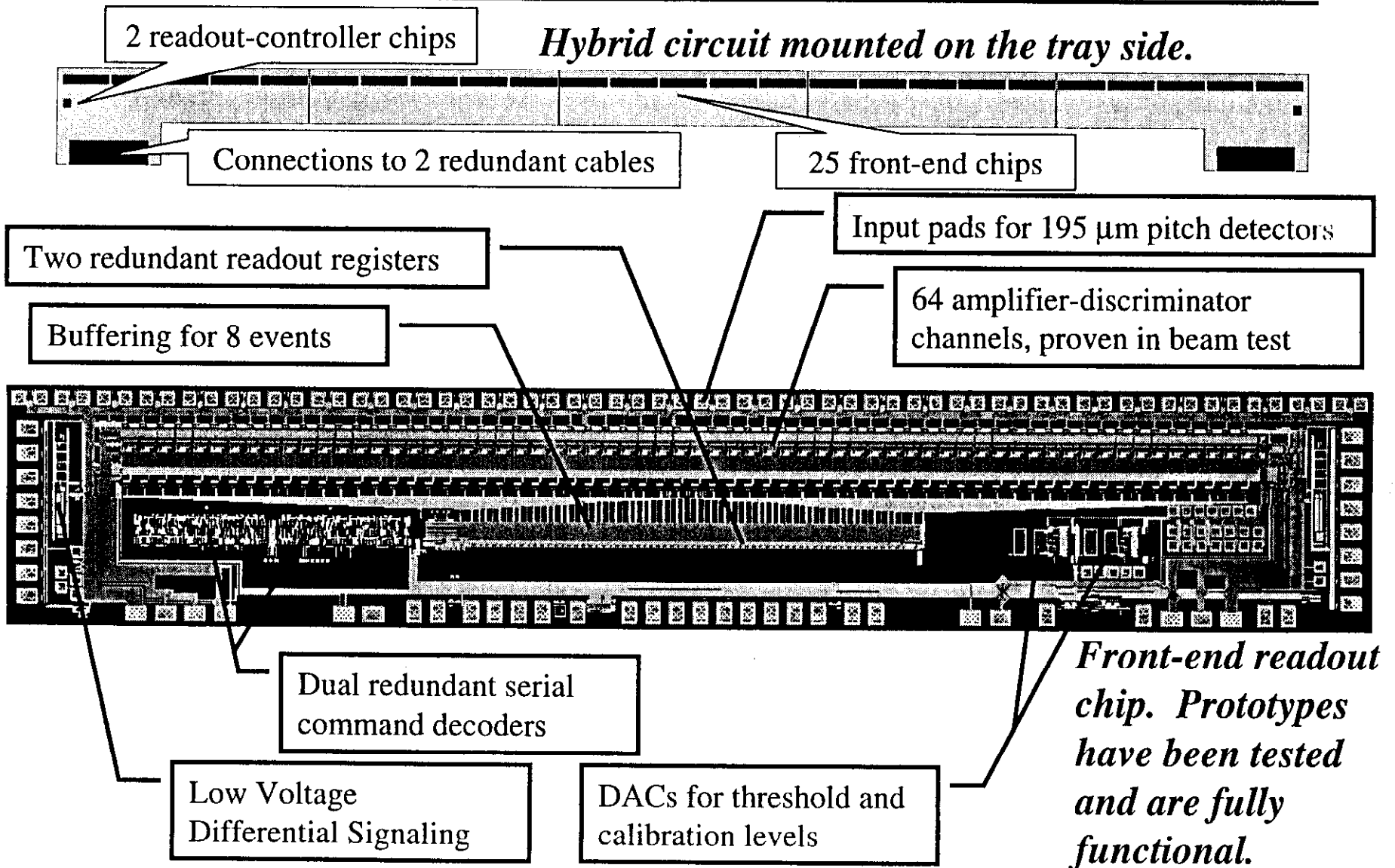
The first fully functional tray, complete with detectors and electronics, will be completed this September.

SLAC has fabricated several prototypes of an earlier design. One of these is now being outfitted with reject detectors for assembly and mechanical studies.





Tracker Readout Electronics

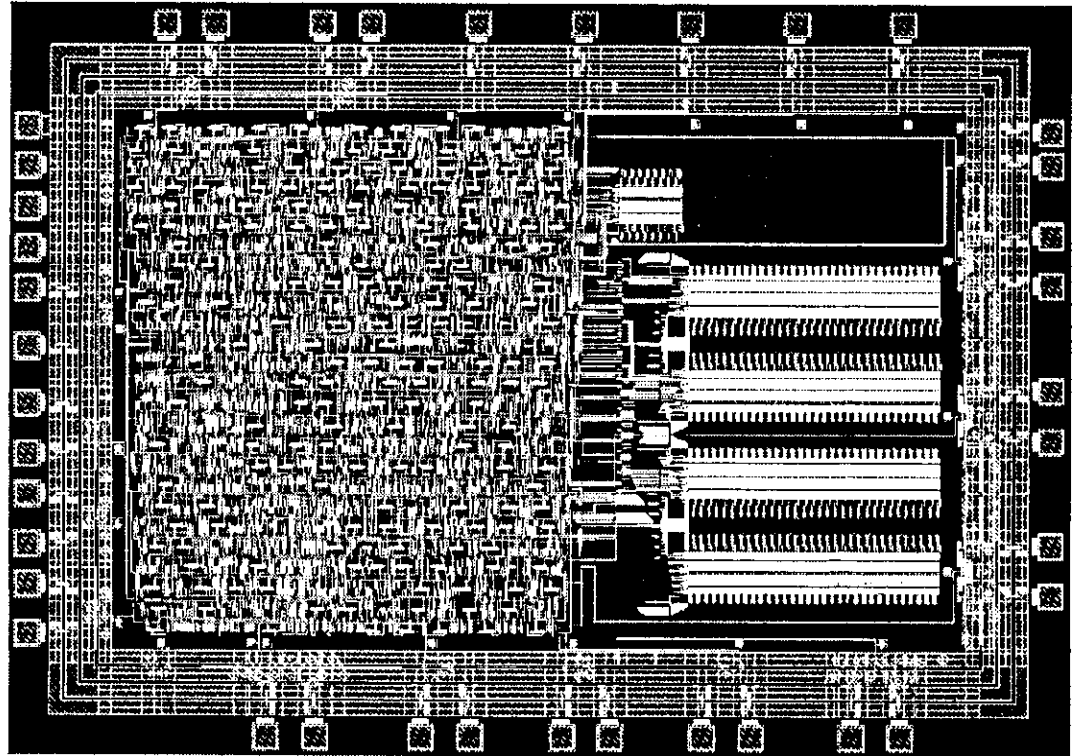




Tracker Electronics, Readout Controller Chip

Readout Controller

- Full custom CMOS IC.
- Logic design and layout uses DoD standard cells.
- *The first prototype has been tested and is fully functional.*
- Functions:
 - Zero suppression and formatting of the data.
 - Command and clock interface to the front-end chips.
 - Sequencing and buffering of the readout.
 - Time-over-threshold of the Fast-OR trigger output.
 - Communication via low-voltage differential signaling (LVDS).

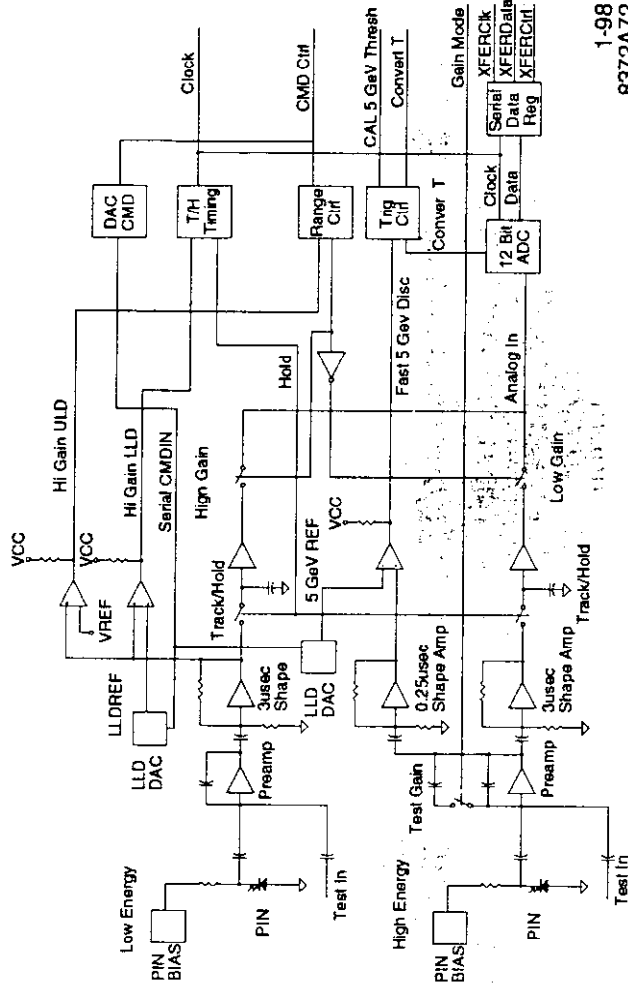


Cadence layout of the GLAST Tracker Readout Controller chip (GTRC). The left half is logic made from standard cells, while the right half is memory for buffering.



Calorimeter Electronics Development

- The main challenge is the large dynamic range requirement (1 MeV to 40 GeV for current prototype specification), together with a small per-channel power allocation.
- A CMOS ASIC front-end is being developed to meet the requirements.
- Each end of a CsI crystal has two PIN diodes, for two gain ranges.
- A separate fast amplifier channel provides a signal that can be used for triggering.
- A first partial prototype is in hand and is undergoing testing.



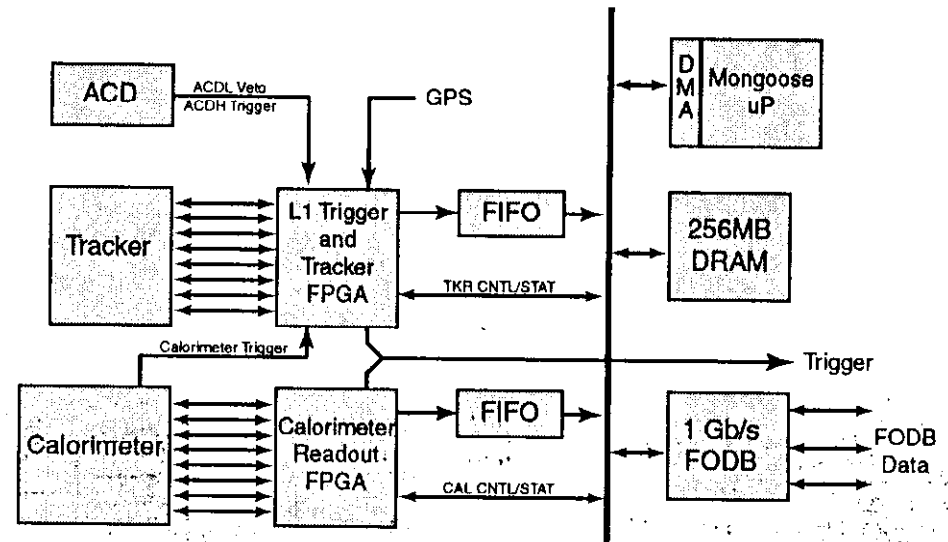
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Conceptual block diagram of the CsI calorimeter front-end electronics.



Data Acquisition Development

- Monte Carlo simulations of rates and detector occupancy.
- Robust level-1 trigger design, using an FPGA.
- Conceptual design of tracker and calorimeter readout, with an average dead time of less than $10\mu\text{s}$ per event.
- Optimization of low-power performance ($<150\text{ W}$ allocated). Continuing investigation of ultra-low power CMOS.
- VME based prototyping system at Stanford. First prototypes to be delivered in September.
- ARGOS flight test bed.



Top level block diagram of the data acquisition system architecture.

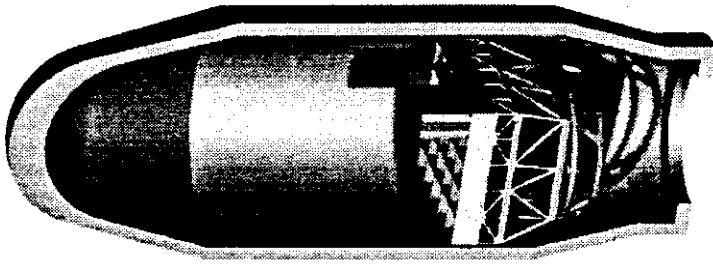


GLAST Mechanical Integration

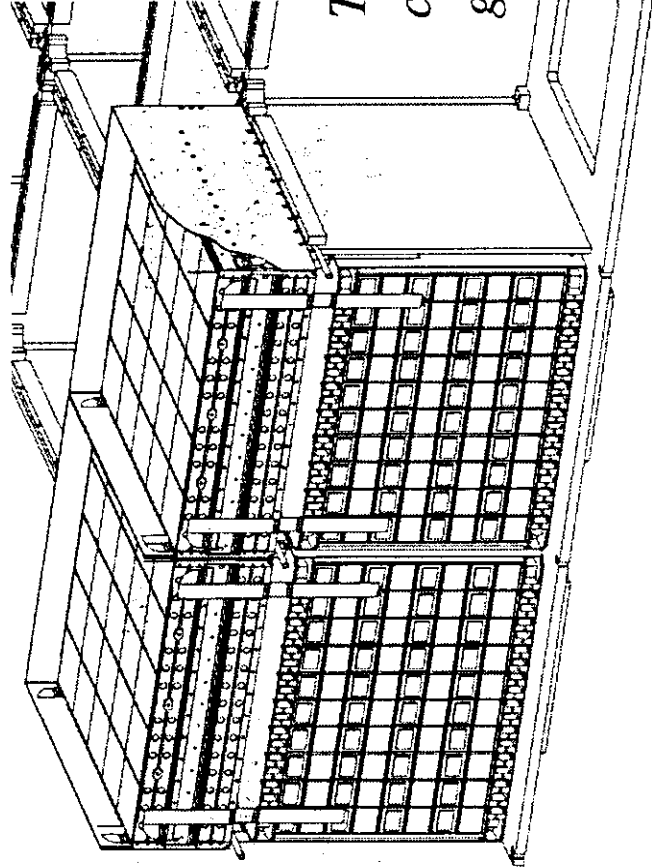
Work is in progress to study the mechanical structure from the spacecraft support ring to the instrument support grid, and on up through the calorimeter, tracker, and ACD.

This includes extensive FEA and dynamical calculations.

The design includes a cooling system involving conductive panels, heat pipes in the support grid, and radiator panels on the exterior.



*Delta-II
Fairing*



*Tracker,
calorimeter,
grid integration*



Conclusions

- The GLAST baseline design has already been well supported by a vigorous R&D program:
 - Extensive simulations for evaluation of the performance and optimization of the design parameters.
 - A conceptual design for an *integrated instrument*, with viable triggering and readout schemes worked out in detail.
 - Successful proof-of-principle test of the critical technology: low power, low noise readout for silicon strip detectors,
 - Highly successful beam test of prototype components from all subsystems.
- The next major milestone is to build and test a complete full-scale tower module.
 - Detailed engineering integration of all subsystems.
 - Development of fabrication procedures.
 - Tests in accelerator beams of the telescope performance and background rejection.
 - Validation of most aspects of the instrument design.
- Increasing attention to the engineering of the full scale 25-tower instrument.