1. Introduction

In this note we analyze the effect on a SLED-II pulse compressor of errors in the lengths of the delay lines in the NLC Test Accelerator design. The length of each of the two delay lines is supposed to be an integer number of half wavelengths (between the reflecting iris and the shorted end), making each delay line resonant. Thermal expansion and contraction, or misadjustment of the adjustable shorting plungers, may cause the delay line lengths to differ from the nominal length, which will affect adversely the amplitude and phase of the SLED-II output pulse. Since the length errors of the two delay lines may differ from one another, we have evaluated both common and differential error modes. We discuss a feedforward tuning algorithm for stabilizing the delay line lengths, and we derive tolerances on the sensors and control actuators necessary to implement it.
2. Symbolic Definitions and Nominal Values

We will use the following symbolic definitions and nominal values from the NLC Test Accelerator design:

- Pulse-compression ratio: $N = 6$
- Round-trip delay-line transit time: $\Delta t = 250 \text{ ns}$
- Delay-line length: $L = 120' = 36.6 \text{ m}$
- Delay-line waveguide (WC475) radius: $a = 4.75''/2 = 0.0603 \text{ m}$
- Round-trip field attenuation of each delay line: $e^{-2\tau} = 0.99$
- Reflection coefficient of each delay line’s iris: $s = 0.684$
- Wavenumber in free-space: $k_0 = 2\pi/(2.62 \text{ cm})$
- Wavenumber in WC475 delay line: $k_g = 2\pi/(2.72 \text{ cm})$
- Linear thermal expansion coefficient: $\kappa = 16.8 \times 10^{-6} /^\circ \text{C}$
- Average temperature change of a delay line: $\Delta T$
- Length changes of the two delay lines: $\Delta L_1, \Delta L_2$
- Phase-length changes of the two delay lines: $\phi_1, \phi_2$
- E-field from klystron: $E_0 = E_0 e^{j\omega t}$
- E-field emitted from iris of $i$th delay line during $n$th time interval of duration $\Delta t$: $E_{e,i}^{(n)} = E_{e,i}^{(n)} e^{j\omega t}; n = 1, \ldots, N$
- E-field of output pulse during $n$th time interval of duration $\Delta t$: $E_{out}^{(n)} = E_{out}^{(n)} e^{j\omega t}$
- E-field reflected back to klystron during $n$th time interval of duration $\Delta t$: $E_{r}^{(n)} = E_{r}^{(n)} e^{j\omega t}; n = 1, \ldots, N$
- E-field of compressed pulse due to delay-line length errors: $E_c = E_{out}^{(n=0)}$
- Rf phase shift of compressed pulse: $\Delta \theta$

3. Thermal Expansion of the Phase Length of a Delay Line

Thermal expansion modifies both the length $L$ and radius $a$ of a delay line by

$$\Delta L = \kappa \Delta TL$$

and

$$\Delta a = \kappa \Delta Ta.$$

Since the wavenumber in the delay line waveguide is

$$k_g = k_0 \sqrt{1 - (3.832/k_0 a)^2},$$

the phase angle $k_g L$ is modified to

$$(k_g + \Delta k_g)(L + \Delta L) = k_0 \sqrt{1 - \left[3.832/k_0 (a + \Delta a)\right]^2} (L + \Delta L)$$

$$\approx k_g L \left[1 + \kappa \Delta T (k_0/k_g)^2\right],$$
so the change in the round-trip phase length of a delay line is
\[ \phi = 2[(k_g + \Delta k_g)(L + \Delta L) - k_g L] \approx 2k_g L \kappa \Delta T (k_0/k_g)^2. \]

An effective length change, \( \Delta L^* \), can be defined that includes the effect of thermal expansion of radius with length:
\[ \Delta L^* = \frac{\phi}{2k_g} = L \kappa \Delta T (k_0/k_g)^2. \]

In the limit of large waveguide radius \((k_0 \alpha \gg 3.832)\), \(k_g \to k_0\) and \(\Delta L^* \to \kappa \Delta T L\), as expected. The 4.75-inch diameter of the delay line is fairly large \((k_0 \alpha = 14.4)\) and the factor \(k_0/k_g\) is 1.037.

The relationships between phase length, effective physical length, and temperature can be summarized as
\[ \frac{\Delta L^*}{0.217 \text{ cm}} = \frac{\Delta L^*}{0.0853 \text{ inch}} = \frac{\Delta T}{3.28^\circ \text{C}} \]
and
\[ \frac{\Delta L^*}{\Delta T} = 0.0661 \text{ cm/}^\circ \text{C} = 0.0260 \text{ inch/}^\circ \text{C}. \]  \(1\)

4. Analysis of SLED-II with Delay-Line Length Errors

Delay-line phase-length errors \((\phi \ell\) where \(\ell = 1, 2)\) will detune the nominally resonant delay lines. The effect of the detuning can be analyzed by adding to the standard SLED-II analysis\(^1\) an imaginary counterpart to the attenuation parameter. We will assume that the length errors are on the scale of an rf wavelength so that the output pulse length, which is the round-trip transit time, is negligibly affected. Under these assumptions, the electric field amplitude emitted from the \(\ell\)th delay line during the \(n\)th time interval of length \(\Delta t\) is given by
\[ E^{(n)}_{e, \ell} = \frac{1 - s^2}{\sqrt{2}} e^{-2\tau + j \phi \ell} [1 + se^{-2\tau + j \phi \ell} + \ldots + (se^{-2\tau + j \phi \ell})^{n-2}] E_0, \text{ for } n \geq 2, \]
\[ E^{(1)}_{e, \ell} = 0, \text{ for } n = 1, \]  \(2\)

which, in the limit of infinitely many round-trip transit time intervals, becomes
\[ \lim_{n \to \infty} E^{(n)}_{e, \ell} = E^{(\infty)}_{e, \ell} = \frac{1 - s^2}{\sqrt{2}} \frac{e^{-2\tau + j \phi \ell}}{1 - se^{-2\tau + j \phi \ell}} E_0. \]  \(3\)

Expression \(3\) can be used to rewrite expression \(2\) in closed form:
\[ E^{(n)}_{e, \ell} = [1 - (se^{-2\tau + j \phi \ell})^{n-1}] E^{(\infty)}_{e, \ell} = \frac{1 - s^2}{\sqrt{2}} \frac{1 - (se^{-2\tau + j \phi \ell})^{n-1}}{1 - se^{-2\tau + j \phi \ell}} e^{-2\tau + j \phi \ell} E_0. \]

In nominal SLED-II operation, the phase of the klystron pulse is reversed at time \(t = (N - 1)\Delta t\), after the delay lines have been filled for \(N - 1\) time intervals. During the \(N\)th
interval, the field amplitude seen at the output is \( \frac{1}{s} \) times the sum of the amplitudes emitted by the delay lines plus the reflection (with coefficient \( s \)) from the irises of the klystron wave amplitude. These amplitudes add constructively in the absence of line-length errors. The electric field amplitude of the SLED-II output (compressed) pulse during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_c = E_{out}^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} + E_{e,2}^{(N)} \right] + sE_0
\]

\[
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ \frac{1}{1 - se^{-2\tau + j\phi_1}}e^{j\phi_1} \right]^{N-1} + \frac{1}{1 - se^{-2\tau + j\phi_2}}e^{j\phi_2} \right] E_0 + sE_0. \quad (4)
\]

The electric field amplitude reflected back toward the klystron through the hybrid during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_r^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} - E_{e,2}^{(N)} \right]
\]

\[
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ \frac{1}{1 - se^{-2\tau + j\phi_1}}e^{j\phi_1} \right]^{N-1} - \frac{1}{1 - se^{-2\tau + j\phi_2}}e^{j\phi_2} \right] E_0. \quad (5)
\]

One can define common and differential error modes which are linear combinations of the independent phase-length errors of the two delay lines. Any pair of independent length errors, \( \phi_1 \) and \( \phi_2 \), can be represented as a linear combination of a common-mode error

\[
\gamma = \frac{\phi_1 + \phi_2}{2},
\]

and a differential-mode error

\[
\delta = \frac{\phi_1 - \phi_2}{2}.
\]

In the common error mode, both the phase lengths of both delay lines change equally by the angle \( \gamma \). In the differential error mode, the phase lengths of the two lines change oppositely by the angle \( \delta \). The compressed and reflected pulse amplitudes can be expressed in terms of the two error modes by substituting

\[
\phi_1 = \gamma + \delta
\]

and
interval, the field amplitude seen at the output is \( \frac{1}{\sqrt{2}} \times \) times the sum of the amplitudes emitted by the delay lines plus the reflection (with coefficient \( s \)) from the irises of the klystron wave amplitude. These amplitudes add constructively in the absence of line-length errors. The electric field amplitude of the SLED-II output (compressed) pulse during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_c = E_{out}^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} + E_{e,2}^{(N)} \right] + sE_0
\]

\[
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ 1 - \frac{(se^{-2\tau+j\phi_1})^{N-1}}{1 - se^{-2\tau+j\phi_1}} e^{j\phi_1} \right. \\
+ \frac{1 - (se^{-2\tau+j\phi_2})^{N-1}}{1 - se^{-2\tau+j\phi_2}} e^{j\phi_2} \left. \right] E_0 + sE_0. \tag{4}
\]

The electric field amplitude reflected back toward the klystron through the hybrid during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_r^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} - E_{e,2}^{(N)} \right]
\]

\[
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ 1 - \frac{(se^{-2\tau+j\phi_1})^{N-1}}{1 - se^{-2\tau+j\phi_1}} e^{j\phi_1} \right. \\
- \frac{1 - (se^{-2\tau+j\phi_2})^{N-1}}{1 - se^{-2\tau+j\phi_2}} e^{j\phi_2} \left. \right] E_0. \tag{5}
\]

One can define common and differential error modes which are linear combinations of the independent phase-length errors of the two delay lines. Any pair of independent length errors, \( \phi_1 \) and \( \phi_2 \), can be represented as a linear combination of a common-mode error

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\[
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\]

and
interval, the field amplitude seen at the output is \( \frac{1}{\sqrt{2}} \) times the sum of the amplitudes emitted by the delay lines plus the reflection (with coefficient \( s \)) from the irises of the klystron wave amplitude. These amplitudes add constructively in the absence of line-length errors. The electric field amplitude of the SLED-II output (compressed) pulse during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_c = E_{\text{out}}^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} + E_{e,2}^{(N)} \right] + s E_0 \\
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ \frac{1 - (s e^{-2\tau+j\phi_1})^{N-1}}{1 - s e^{-2\tau+j\phi_1}} e^{j\phi_1} \\
+ \frac{1 - (s e^{-2\tau+j\phi_2})^{N-1}}{1 - s e^{-2\tau+j\phi_2}} e^{j\phi_2} \right] E_0 + s E_0.
\]

(4)

The electric field amplitude reflected back toward the klystron through the hybrid during the \( N \)th time interval of duration \( \Delta t \) is

\[
E_r^{(N)} = \frac{1}{\sqrt{2}} \left[ E_{e,1}^{(N)} - E_{e,2}^{(N)} \right] \\
= \frac{(1 - s^2)e^{-2\tau}}{2} \left[ \frac{1 - (s e^{-2\tau+j\phi_1})^{N-1}}{1 - s e^{-2\tau+j\phi_1}} e^{j\phi_1} \\
- \frac{1 - (s e^{-2\tau+j\phi_2})^{N-1}}{1 - s e^{-2\tau+j\phi_2}} e^{j\phi_2} \right] E_0.
\]

(5)

One can define common and differential error modes which are linear combinations of the independent phase-length errors of the two delay lines. Any pair of independent length errors, \( \phi_1 \) and \( \phi_2 \), can be represented as a linear combination of a common-mode error

\[
\gamma = \frac{\phi_1 + \phi_2}{2},
\]

and a differential-mode error

\[
\delta = \frac{\phi_1 - \phi_2}{2}.
\]

In the common error mode, both the phase lengths of both delay lines change equally by the angle \( \gamma \). In the differential error mode, the phase lengths of the two lines change oppositely by the angle \( \delta \). The compressed and reflected pulse amplitudes can be expressed in terms of the two error modes by substituting

\[
\phi_1 = \gamma + \delta
\]

and
\[ \phi_2 = \gamma - \delta \]

into equations (4) and (5).

**Common Error Mode:**

In the pure common mode \((\phi_1 = \phi_2, \delta = 0)\), the compressed and reflected pulse amplitudes will be

\[ E_c = E_{out}^{(N)} = (1 - s^2) \frac{1 - (se^{-2\tau + j\gamma})^{N-1}}{1 - se^{-2\tau + j\gamma}} e^{-2\tau + j\gamma} E_0 + s E_0 \]

and

\[ E_r^{(N)} = 0. \]

\(E_c\) in general is complex in the presence of delay-line length errors because successive reflections in the delay lines are out of phase. Consequently, the phase of \(E_c\) will be shifted with respect to \(E_0\). There will be no reflection \(E_r\) when the lengths of the delay lines are identical and do not break the symmetry of the network, which is designed to direct all power away from the source. Figure 1 shows the SLED-II compressed-pulse power gain and phase shift in response to a common-mode delay-line length error.

**Differential Error Mode:**

In the pure differential mode \((\phi_1 = -\phi_2, \gamma = 0)\),

\[ E_c = E_{out}^{(N)} = (1 - s^2) e^{-2\tau} \text{Real} \left[ \frac{1 - (se^{-2\tau + j\delta})^{N-1}}{1 - se^{-2\tau + j\delta}} e^{j\delta} \right] E_0 + s E_0 \]

and

\[ E_r^{(N)} = (1 - s^2) e^{-2\tau} \text{Imag} \left[ \frac{1 - (se^{-2\tau + j\delta})^{N-1}}{1 - se^{-2\tau + j\delta}} e^{j\delta} \right] E_0. \]

The compressed pulse amplitude \(E_c\) is pure real, in phase with the klystron wave amplitude \((E_0)\), so the output wave \(E_c\) is not phase-shifted with respect to the input wave \(E_0\). The reflected wave amplitude \(E_r\) is non-zero. Figure 2 shows the SLED-II compressed and reflected power in response to a differential-mode delay-line length error. Figure 3 shows the reflected power response on a log-log scale.
5. An Algorithm for Fine-Tuning the Delay-Line Lengths

The preceding analysis indicates diagnostic signals that can be used for fine-tuning the lengths of the delay lines during operation. A common-mode error is indicated uniquely by an output phase shift. A differential-mode error is indicated by power being reflected back to the klystron. (We assume that other matching errors and their reflections are sufficiently small so that they are not confused with the differential-mode delay-line error.)

A reasonable tuning algorithm might consist of, first, adjusting the delay-line lengths differentially to minimize the power reflected back to the klystron, and, second, adjusting the delay-line lengths in common to minimize the output phase shift. Figure 4 shows a conceptual block diagram of the relationships between the sensors and control actuators.

The output phase shift that uniquely indicates a common-mode delay-line length error can be detected by measuring the phase of the compressed output pulse ($E_c = E_{out}^{(N)}$) relative to the low-level rf oscillator during the $N$th time interval. This measurement is calibrated using the phase of the output pulse relative to the low-level rf oscillator during the first time interval ($E_{out}^{(1)}$). This works because $E_{out}^{(1)}$ results purely from the reflections off the two SLED-II coupling irises (which we will assume are equal) and contains no component emitted from the delay lines, so the phase of $E_{out}^{(1)}$ is unaffected by delay-line length errors. Consequently, if the phase of $E_{out}^{(1)}$ is "zeroed" by adjusting the variable phase shifter, then the phase measurement of $E_c$ will be calibrated: a non-zero measurement will indicate a common-mode delay-line length error.

Used as a feedforward loop (meaning that corrections are applied to future pulses) with a time constant matched to the thermal expansions of the delay lines, such a tuning algorithm should suffice to maintain the delay-line lengths at their nominal operating set-points. Adjustment of the calibration phase shifter can be performed continuously by a separate feedforward loop. Table 1 summarizes the various sensors and control actuators participating in the system described above.

Quantitative considerations for the tolerances of the sensors are discussed in the next section.

The above tuning algorithm should also be helpful for finding the operating set-points from a random initial state that is within approximately 0.04" of differential-mode error, and 0.06" of common-mode error, from the nominal operating state. These tolerances are much looser than the tolerances for nominal operation, and apply only to the set of states that can be "captured" and fine-tuned to the nominal operating state.

Initial states that lie outside the above capture tolerances can be adjusted to within the capture tolerances by a human operator. Illustrations of such problematic states are discussed by Farkas in Reference 2.
Table 1. Summary of the sensors and control actuators used to stabilize adjustable shorts

<table>
<thead>
<tr>
<th>Sensor</th>
<th>Sample Rate</th>
<th>Time Resol.</th>
<th>Meaning of Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reflected power</td>
<td>PRF*</td>
<td>&lt; 0.2 µs</td>
<td>Nonzero indicates dif.-mode error</td>
</tr>
<tr>
<td>Output phase in 1st bin</td>
<td>0.1 Hz</td>
<td>&lt; 0.2 µs</td>
<td>Nonzero if phase shifter is uncal.</td>
</tr>
<tr>
<td>Output phase in last bin</td>
<td>PRF*</td>
<td>&lt; 0.2 µs</td>
<td>Nonzero indicates com.-mode error</td>
</tr>
<tr>
<td>Compressed power</td>
<td>PRF*</td>
<td>&lt; 0.2 µs</td>
<td>Max. when errors are eliminated</td>
</tr>
<tr>
<td>LVDT (linear position)</td>
<td>0.1 Hz</td>
<td>&lt; 1 s</td>
<td>Readback position of short</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Actuator</th>
<th>Freq. of Adj'ment</th>
<th>Settle time</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>Phase shifter for calib.</td>
<td>0.03 Hz</td>
<td>&lt; 10 s</td>
<td>Zeroes detected phase in Bin 1</td>
</tr>
<tr>
<td>Stepper motors</td>
<td>0.03 Hz</td>
<td>&lt; 10 s</td>
<td>Com.- &amp; dif.-mode adjustments</td>
</tr>
</tbody>
</table>

*"PRF" is the pulse repetition frequency, which nominally is 60 Hz.

6. Precision and Duty Cycle of Tuning Sensors and Actuators

Delay-line length errors remaining after tuning will reduce the peak amplitude of the accelerating field ($E_c$) and, in the common error mode, they will shift the phase ($\theta$) of the rf wave from its nominal value of $5^\circ$ off-crest. Assuming a nominal accelerating gradient of $E_c \cos 5^\circ$, we have plotted in Figure 5 the reduction in accelerating gradient,

$$1 - \frac{E_c(\Delta L) \cos (5^\circ + \Delta \theta(\Delta L))}{E_c(0) \cos 5^\circ},$$  \hspace{1cm} (6)

for both the common and differential error modes of a single SLED-II pulse compressor. For differential-mode errors, $\Delta \theta$ is zero and expression (6) reduces to

$$1 - \frac{E_c(\Delta L)}{E_c(0)}. \hspace{1cm} (7)$$

How precisely must one be able to measure rf phase (relative to reference) in order to tune out a common-mode delay-line length error $\Delta L$? Figure 1 shows that the sensitivity of the output phase shift $\Delta \theta$ to a small common-mode length error $\Delta L$ is $\Delta \theta/\Delta L = 1100^\circ$/inch. If the fractional reduction in accelerating gradient due to common-mode length errors is to be kept less than 0.5%, then Figure 5 indicates that common-mode length errors must be made less than 0.002". Consequently, one must be able to move an adjustable shorting
plunger actuator by 0.002" and to measure relative rf phase with an accuracy of at least
1100°/inch x 0.002 inch = 2.2° near 0° in order to adequately tune out a common-mode delay-
line length error. If thermal stabilization is used for length adjustment, instead of adjustable
shorting plungers, then equation (1) implies that the temperature stability equivalent to the
0.002" length stability is 0.077°C.

How precisely must one be able to measure reflected rf power in order to tune out a
differential-mode length error ΔL? If the fractional reduction in accelerating gradient due
to differential-mode length errors is to be kept less than 0.5%, then Figure 5 indicates that
differential-mode length errors must be made less than 0.004". Consequently, one must be
able to move an adjustable shorting plunger actuator by 0.004" (which is a looser tolerance
than that imposed by the common-mode case) and to measure reflected power relative to
klystron power (Figure 3) with a relative precision of 2% (1 MW reflected back to a 50-MW
klystron) in order to adequately tune out a differential-mode delay-line length error.

How often must the delay line lengths be re-adjusted? Assuming the rate of temperature
change inside End Station B is less than 1°C/h (consistent with preliminary measurements
by R. Fuller) the delay line lengths must be re-adjusted at least every 280 seconds in order
to achieve the 0.077°C temperature stability necessary for 0.5% gradient stability.

Following the above examples, which were for 0.5% gradient stability, Table 2 indicates
the minimum precision and duty cycle of the sensors and actuators that is necessary to
achieve several different levels of gradient stability. The sensor and actuator precisions
quoted in Table 2 have been chosen such that the desired gradient stability will be exceeded
in one step; actual tolerances on the sensors and actuators would have to be several times
tighter than the values in Table 2 for a computerized control algorithm to be able to bracket
the desired operating point.

| Table 2. Minimum precision and duty cycle of sensors and actuators
necessary to achieve different levels of accelerating-gradient stability |
<table>
<thead>
<tr>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Accelerating-gradient stability</td>
</tr>
<tr>
<td>Linear actuator step size</td>
</tr>
<tr>
<td>Equivalent temperature stability (°C)</td>
</tr>
<tr>
<td>Phase resolution near 0°</td>
</tr>
<tr>
<td>Resol. of reflected power rel. to klystron power</td>
</tr>
<tr>
<td>Time between corrections*</td>
</tr>
</tbody>
</table>

*Assuming temperature change of 1°C/h.

For example, to achieve the 0.1% level of accelerating-gradient stability, one should strive
for approximately 0.0002" step-size, 0.2° phase-detection resolution, 0.2% reflected power
resolution (relative to klystron power), and 30 seconds between corrections. Phase and
amplitude detectors in use at the SLC already provide approximately 0.1° phase resolution, 0.1% power resolution, and 30-ns time resolution (15-MHz bandwidth) for a single sample per rf pulse, indicating that it should be possible to achieve the tolerances necessary for 0.1% gradient stability from SLED-II at the NLCTA by using feedforward correction of the adjustable shorts, even without precise temperature regulation of the 120-foot-long delay lines. We should plan to do this well.

Other limitations besides SLED-II delay-line length stability may arise when trying to achieve accelerating-gradient stability better than a percent. Phase ripple in the klystron pulse caused, for example, by voltage ripple in the modulator pulse, may limit the ability to precisely tune the delay-line lengths. Some type of klystron phase-stabilization feedforward control may be needed to monitor the phase of the klystron output pulse in intervals of several nanoseconds each and modify the phase of the low-level rf drive waveform in corresponding intervals so as to correct the klystron output phase on future pulses.

REFERENCES


2. Z. D. Farkas, “Tuning SLED-II” (SLAC-AAS-Note 79), July 1993.

FIGURE CAPTIONS

1) SLED-II compressed-pulse power gain and phase shift in response to a common-mode delay-line length error.

2) SLED-II compressed and reflected power in response to a differential-mode delay-line length error.

3) Log-log plot of the reflected power in response to a differential-mode delay-line length error.

4) Conceptual block diagram of the delay-line length tuning components.

5) Delay-line length error tolerances.
Figure 1.